

In the Claims

Please amend claims 1, 9, 19, 21, and 24 as follows.

1. (Currently Amended) A method for representing a root bus of a computer system, comprising:

~~creating a globally unique identifier (GUID) for the root bus;~~

~~defining~~ dynamically generating an object-oriented abstraction corresponding to the root bus ~~comprising a set of components that includes reference to~~ referencing one or more methods that may be implemented to obtain and/or generate configuration and resource allocation information for the root bus and any subordinate busses connected either directly or indirectly to the root bus; and

registering the methods referenced in the object-oriented abstraction via a data structure stored in memory of the computer system.

2. (Original) The method of claim 1, wherein the object-oriented abstraction comprises one of a C++ object or Java object.

3. (Original) The method of claim 1, wherein the root bus comprises a PCI bus.

4. (Original) The method of claim 1, further comprising enumerating the root bus and said any subordinate busses through use of the methods that are registered.

5. (Original) The method of claim 4, further wherein the set of components of the object-oriented abstraction include at least one variable for storing information, further

comprising storing configuration information derived during enumeration of the root bus into said at least one variable.

6. (Original) The method of claim 5, further comprising allocating resources for the root bus, each subordinate bus, and any devices attached to those root and subordinate busses; and

storing information corresponding to resources that are allocated in said at least one variable for storing information.

7. (Original) The method of claim 1, wherein functions of the root bus are controlled, at least in part, by a chipset having a plug-in driver, further comprising interrogating the plug-in driver to identify said plurality of methods.

8. (Original) The method of claim 1, wherein functions of the root bus are controlled, at least in part, by a chipset having a plug-in driver, further comprising publishing the object-oriented abstraction via the plug-in driver.

9. (Currently Amended) A method for defining resource configuration information in a system that includes a plurality of root busses, comprising:

identifying each of the plurality of root busses;

defining an object oriented representation of each root bus comprising a set of components that includes references to a plurality of methods that may be implemented to obtain and/or generate configuration and resource allocation information for that root bus and any subordinate busses connected either directly or indirectly to the root bus;

assigning a bus identifier for each of the subordinate busses through use of an enumeration process that implements one or more of the methods referenced by the object oriented representation of that root bus,

wherein each of the foregoing operations is performed via execution of machine-executable instructions by the system.

10. (Original) The method of claim 9, wherein the object oriented representation includes a globally unique identifier (GUID) for each root bus.

11. (Original) The method of claim 10, further comprising:
creating a handle; and
storing references corresponding to the GUIDs for each root bus in the handle.

12. (Original) The method of claim 11, wherein the handle further includes indicia for each GUID identifying a location of the object oriented representation corresponding to the GUID.

13. (Original) The method of claim 12, wherein the indicia comprises a pointer to the memory address at which the object oriented representation is stored.

14. (Original) The method of claim 9, wherein each root bus and any subordinate busses connected either directly or indirectly to the root bus form a hierarchy, and wherein the enumeration process for each root bus comprises:

assigning bus identifiers as subordinate busses are reached while moving downward through the hierarchy; and

calculating resource requirements for each subordinate bus while moving back up the hierarchy.

15. (Original) The method of claim 9, further comprising:

determining resource requirements for each subordinate bus; allocating the resource requirements for that subordinate bus; and

setting resources for that subordinate bus.

16. (Original) The method of claim 15, wherein at least one of the subordinate busses for a given root bus has a peripheral device connected to it, and further wherein determining the resource requirements for each subordinate bus includes determining the resource requirements of any peripheral devices attached to that subordinate bus.

17. (Original) The method of claim 15, further comprising:

allocating resources for each root bus based in part on the resources of its subordinate busses; and

setting the resources for that root bus.

18. (Original) The method of claim 9, further comprising:

evaluating devices in the hierarchy of each root bus to determine if they produce a firmware device or an optional ROM that may include BIOS corresponding to a bootable device.

19. (Currently Amended) An article of manufacture comprising a computer-readable medium having computer-executable instructions that when executed perform the functions of:

~~creating a globally unique identifier (GUID) for the root bus;~~

~~defining~~ generating an object-oriented abstraction corresponding to ~~[[the]]~~ a root bus ~~comprising a set of components that includes reference to~~ referencing one or more methods that may be implemented to obtain and/or generate configuration and resource allocation information for the root bus and any subordinate busses connected either directly or indirectly to the root bus; and

registering the methods referenced in the object-oriented abstraction.

20. (Original) The article of manufacture of claim 19, wherein the computer-executable instructions comprises one or more software modules including a root bus driver.

21. (Currently Amended) The article of manufacture of claim 19, wherein execution of the instructions further performs the function of assigning a bus identifier for each of the subordinate busses through use of an enumeration process that implements one or more of the methods referenced by the object oriented ~~representation~~ abstraction of that root bus.

22. (Original) The article of manufacture of claim 21, wherein the root bus and any subordinate busses connected either directly or indirectly to the root bus form a hierarchy, and wherein the enumeration process for the root bus comprises:

assigning bus identifiers as subordinate busses are reached while moving downward through the hierarchy; and

calculating resource requirements for each subordinate bus while moving back up the hierarchy.

23. (Original) The article of manufacture of claim 22, wherein execution of the instructions further performs the functions of:

determining resource requirements for each subordinate bus;

allocating the resource requirements for that subordinate bus; and

assigning the resources that are allocated to the root bus that is a parent of that subordinate bus.

24. (Currently Amended) The article of manufacture of claim 19, wherein execution of the instructions further performs the functions of:

creating a handle; and

storing references corresponding to a [[GUID]] globally unique identifier (GUID) for the object-oriented abstraction and a pointer to the object-oriented abstraction in the handle.